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<p>Sheet</p>		<p>1</p>	<p>of</p>	<p>2</p>	<p>Application Number</p>
					<p>Filing Date</p>
					<p>First Named Inventor</p>
					<p>Art Unit</p>
					<p>Examiner Name</p>
					<p>Attorney Docket Number</p>

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature	Shamim Ahmed	Date Considered	8/4/05
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				Application Number	
				Filing Date	
				First Named Inventor	Soon-Yong Kweon
				Art Unit	
				Examiner Name	
				Attorney Docket Number	51876P453
Sheet	2	of	2		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
SA		Y. NAGANO, et al., 0.18um SBT-Based Embedded FeRAM Operating at a Low Voltage of 11V, 2003 Symposium on VLSI Technology Digest of Technical Papers	

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